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# GT25C16

Hold timing.

**Note:** \_\_\_\_\_ cannot be powered on earlier than Vcc, and



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the WPEN bit. The values of the other data bits incorporated into WRSR can be 0 or 1 and are not stored in the Status Register. WRSR will be ignored unless both following conditions are true: a) WEN = 1, due to a prior







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# GT25C16

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## 8. Top Markings

### 8.1 SOIC Package

**G:** Giantec Logo  
516-2GLI: GT25C16-









