GT24C04B

Copyright © 2014 Giantec Semiconductor Corp. (Giantec). All rights reserved. Giantec reserves the right to make changes to th

GT24C

4.4 Pin Descriptions

SCL

This input clock pin is used to synchronize the data transfer to and from the device.

SDA

The SDA is a bi-directional pin used to transfer addresses and data into and out of the device. The SDA pin is an open drain output and can be wired with other open drain or open collector outputs. However, the SDA pin requires a pull-up resistor connected to the power supply.

WP

WP is the Write Protect pin. While the WP pin is connected to the power supply of GT24C04B,

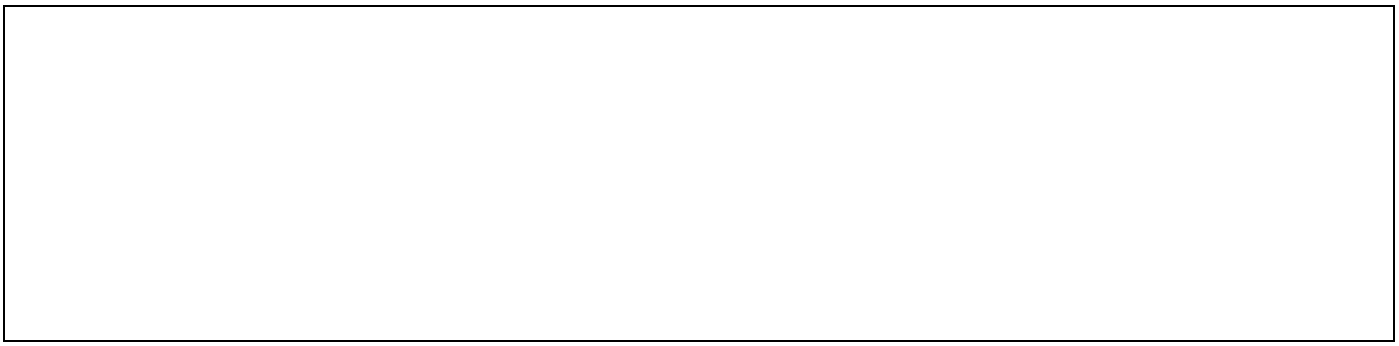
5. Device Operation

The GT24C04B serial interface supports communications using the standard 2-wire bus protocol, such as I^2C.

5.1 2-WIRE Bus

The two-

Figure 7. Page Write



5.9.3 Acknowledge (ACK) Polling

GT24C04B

8. Ordering Information

Voltage Range	Part Number*	