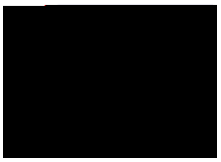

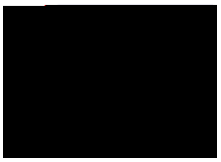

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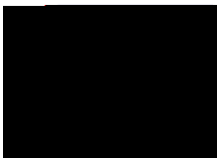
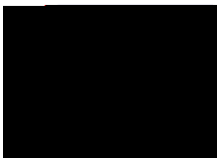




6. Device Features

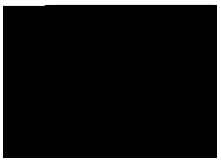
6.1

6.3 Software Write Protect

In order to prevent unexpected write sequence, this device offers the SWP feature, which makes it possible to protect the whole memory conid7(i)-4(c)7(pe)-3(q)-r fio(q)-3(u)9(th)7(i)-4aat d-3(,)-3(th)7a(s)7(s)-6(i)9(b94(s)-6 (t)1)7a(s)d042 Tc[(le)] TJETQ EMC /Sp9n A

7. Application Recommendation

7.1 Operating Supply Voltage



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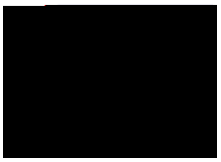

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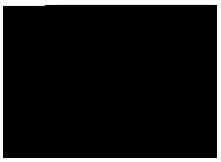
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